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United States Patent [19]

Kao et al.

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[54] **SEMICONDUCTOR WAFER HAVING A
BOTTOM SURFACE PROTECTIVE
COATING**

63-285955 11/1988 Japan 257/790
4-116955 4/1992 Japan 257/797

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[57] **ABSTRACT**

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[51] **Int. Cl.**⁷ **H01L 23/58**

[52] **U.S. Cl.** **257/632; 257/783; 257/797**

[58] **Field of Search** **257/797, 790,
257/786, 783, 632**

Disclosed is a packaged integrated circuit device. The device includes a die having a plurality of electrical contacts on a first surface of the die and a protective film adhered directly to a back surface of the die, the protective film being thick enough to allow laser marking of the protective film without the laser penetrating to the die. In one preferred embodiment, the protective film of the device is a thick film formed by screen printing. In a preferred embodiment, the protective film has a thickness of between about 1.5 and 5 mils. Also, disclosed is a method of fabricating a semiconductor wafer having a wafer substrate with a top surface and a bottom surface and a plurality of dies. In this embodiment, the method includes providing a plurality of dies on the top surface of the wafer substrate, applying a thick film over the bottom surface of the wafer substrate, adhering the thick film to a mounting tape that is not ultraviolet curable, and dicing the wafer to separate the dies. The thick film reduces chipping along edges of the separated dies.

[56] **References Cited****U.S. PATENT DOCUMENTS**

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6 Claims, 3 Drawing Sheets